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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit 2182
Examiner Peyton, Tammara R.

In Re: Mario Nemirovsky et al.
Case: P3814
Serial No.: 09/602,279
Filed: 06/23/00
Subject: Methods and Apparatus for Background Memory Management

To: The Commissioner of Patents and Trademarks
Washington, D.C. 20231

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FEB 25 2003

Technology Center 2100

Dear Sir;

Response A

All of the claims standing for examination are reproduced below. Those claims herein amended are marked (Amended). Those claims left unchanged are marked (Unchanged).

1. (Unchanged) A background memory manager (BMM) for managing a memory in a data processing system, the BMM comprising:

circuity for transferring data to and from an outside device and to and from a memory;

a memory state map associated with the memory; and

a communication link to a processor;

characterized in that the BMM manages the memory, determining if each data structure fits into the memory, deciding exactly where to place the data